



FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
ARC.005RC2APPLICATION NO.
09/523,877INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT
Peter Warnes, et al.FILING DATE
March 13, 2000GROUP
2183

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE (IF APPROPRIATE) |
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| DJH | 1 | 5,493,508 | 2/20/96 | Dangelo, et al. | | | |
| DJH | 2 | 5,535,331 | 7/9/96 | Swoboda et al. | | | |
| DJH | 3 | 5,544,067 | 8/6/96 | Rostoker, et al. | | | |
| DJH | 4 | 5,555,201 | 9/10/96 | Dangelo, et al. | | | |
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| DJH | 6 | 5,812,416 | 9/22/98 | Gupke, et al. | | | |
| DJH | 7 | 5,867,399 | 02/1999 | Rostoker, et al. | | | |
| DJH | 8 | 6,173,434 | 01/2001 | Wirthlin, et al. | | | |
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| DJH | 10 | 6,378,123 | 04/2002 | Dupenloup | | | |
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| | | | | | | | YES | NO |
| DJH | 17 | 2 308 470 | 6/97 | GB | | | | |
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| DJH | 19 | Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039 |
| DJH | 20 | Tensilica, Inc; Xensa Instruction Set Architecture (ISA) Reference Manual Revision 1.0. (First 25 pages) |
| DJH | 21 | Tensilica, Inc. Instruction Extension Language (TIE) Reference Manual Revision 1.3. |
| DJH | 22 | Compton et al., "Configurable Computing: A Survey of Systems and Software," Technical Report, Northwestern University, Dept. of ECE, 1999. |
| DJH | 23 | Yang, Jin-Hyuk, et al., "MetaCore: A Configurable & Instruction-Level Extensible DSP Core," Proceedings of the ASP-DAC '98 Asian and South Pacific Design Automation Conference 1998, pages 325-326, XP-002137268 |

EXAMINER

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DATE CONSIDERED

9-7-15

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